Fpga Implementation Of Lte Downlink Transceiver With

This book focuses on the current hottest issues from the lowest layers to the upper layers of wireless communication networks and provides "real-time" research progress on these issues. The authors have made every effort to systematically organize the information on these topics to make it easily accessible to readers of any level. This book also maintains the balance between current research results and their theoretical support. In this book, a variety of novel techniques in wireless communications and networks are investigated. The authors attempt to present these topics in detail. Insightful and reader-friendly descriptions are presented to nourish readers of any level, from practicing and knowledgeable communication engineers to beginning or professional researchers. All interested readers can easily find noteworthy materials in much greater detail than in previous publications and in the references cited in these chapters. This book focuses on domain-specific heterogeneous reconfigurable architectures, demonstrating for readers a computing platform which is flexible enough to support multiple standards, multiple modes, and multiple algorithms. The content is multi-disciplinary, covering areas of wireless communication, computing architecture, and circuit design. The platform described provides real-time processing capability with reasonable implementation cost, achieving balanced trade-offs among flexibility, performance, and hardware costs. The authors discuss efficient design methods for wireless communication processing platforms, from both an algorithm and architecture design perspective. Coverage also includes computing platforms for different wireless technologies and standards, including MIMO, OFDM, Massive MIMO, DVB, WLAN, LTE/LTE-A, and 5G.

This book highlights the latest research findings, methods and techniques, as well as challenges and solutions related to Ubiquitous and Pervasive Computing (UPC). In this regard, it employs both theoretical and practical perspectives, and places special emphasis on innovative, mobile and internet services. With the proliferation of wireless technologies and electronic devices, there is a rapidly growing interest in Ubiquitous and Pervasive Computing (UPC). UPC makes it possible to create a human-oriented computing environment in which computer chips are embedded in everyday objects and interact with the physical world. Through UPC, people can remain online even while underway, thus enjoying nearly permanent access to their preferred services. Though it has a great potential to revolutionize our lives, UPC also poses a number of new research challenges.

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A reliable and focused treatment of the emergent technology of fifth generation (5G) networks This book provides an understanding of the most recent developments in 5G, from both theoretical and industrial perspectives. It identifies and discusses technical challenges and recent results related to improving capacity and spectral efficiency on the radio interface side, and operations management on the core network side. It covers both existing network technologies and those currently in development in three major areas of 5G: spectrum extension, spatial spectrum utilization, and core network and network topology management. It explores new spectrum opportunities; the capability of radio access technology; and the operation of network infrastructure and heterogeneous QoE provisioning. 5G Networks: Fundamental Requirements, Enabling Technologies, and Operations Management is split into five sections: Physical Layer for 5G Radio Interface Technologies; Radio Access Technology for 5G Networks; 5G Network Interworking and Core Network Advancements; Vertical 5G Applications; and R&D and 5G Standardization. It starts by introducing emerging technologies in 5G software, hardware, and management aspects before moving on to cover waveform design for 5G and beyond; code design for multi-user MIMO; network slicing for 5G networks; machine type communication in the 5G era; provisioning unlicensed LAA interface for smart grid applications; moving toward all-IT 5G end-to-end infrastructure; and more. This valuable resource: Provides a comprehensive reference for all layers of 5G networks Focuses on fundamental issues in an easy language that is understandable by a wide audience Includes both beginner and advanced examples at the end of each section Features sections on major open research challenges 5G Networks: Fundamental Requirements, Enabling Technologies, and Operations Management is an excellent book for graduate students, academic researchers, and industry professionals, involved in 5G technology.

This book constitutes the refereed proceedings of the First International Conference on Advanced Hybrid Information Processing, ADHIB 2017, held in Harbin, China, in July 2017. The 64 full papers were selected from 134 submissions and focus on advanced methods and applications for hybrid information processing.

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LTE-Advanced relay technology and standardization provides a timely reference work for relay technology with the finalizing of LTE Release 10 specifications. LTE-Advanced is quickly becoming the global standard for 4G cellular communications. The relay technology, as one of the key features in LTE-Advanced, helps not only to improve the system coverage and capacity, but also to save the costs of laying wireline backhaul. As a leading researcher in the field of LTE-Advanced standards, the author provides an in-depth description of LTE-A relay technology, and explains in detail the standard specification and design principles. Readers from both academic and industrial fields can find sections of interest to them: Sections 2 & 4 could benefit researchers in academia and those who are engaged in exploratory work, while Sections 3 & 4 are more useful to engineers. Dr. Yifei Yuan is the Technical Director at the Standards Department of ZTE Inc.

This book is about the Zynq-7000 All Programmable System on Chip, the family of devices from Xilinx that combines an application-grade ARM Cortex-A9 processor with traditional FPGA logic fabric. Catering for both new and experienced readers, it covers fundamental issues in an accessible way, starting with a clear overview of the device architecture, and an introduction to the design tools and processes for developing a Zynq SoC. Later chapters progress to more advanced topics such as embedded systems development, IP block design and operating systems. Maintaining a 'real-world' perspective, the book also compares Zynq with other device alternatives, and considers end-user applications. The Zynq Book is accompanied by a set of practical tutorials hosted on a companion website. These tutorials will guide the reader through first steps with Zynq, following on to a complete, audio-based embedded systems design.

The book, presenting the proceedings of the 2018 Future Technologies Conference (FTC 2018), is a remarkable collection of chapters

covering a wide range of topics, including, but not limited to computing, electronics, artificial intelligence, robotics, security and communications and their real-world applications. The conference attracted a total of 503 submissions from pioneering researchers, scientists, industrial engineers, and students from all over the world. After a double-blind peer review process, 173 submissions (including 6 poster papers) have been selected to be included in these proceedings. FTC 2018 successfully brought together technology geniuses in one venue to not only present breakthrough research in future technologies but to also promote practicality and applications and an intra- and inter-field exchange of ideas. In the future, computing technologies will play a very important role in the convergence of computing, communication, and all other computational sciences and applications. And as a result it will also influence the future of science, engineering, industry, business, law, politics, culture, and medicine. Providing state-of-the-art intelligent methods and techniques for solving real-world problems, as well as a vision of the future research, this book is a valuable resource for all those interested in this area.

A practical guide to LTE design, test and measurement, this new edition has been updated to include the latest developments This book presents the latest details on LTE from a practical and technical perspective. Written by Agilent's measurement experts, it offers a valuable insight into LTE technology and its design and test challenges. Chapters cover the upper layer signaling and system architecture evolution (SAE). Basic concepts such as MIMO and SC-FDMA, the new uplink modulation scheme, are introduced and explained, and the authors look into the challenges of verifying the designs of the receivers, transmitters and protocols of LTE systems. The latest information on RF and signaling conformance testing is delivered by authors participating in the LTE 3GPP standards committees. This second edition has been considerably revised to reflect the most recent developments of the technologies and standards. Particularly important updates include an increased focus on LTE-Advanced as well as the latest testing specifications. Fully updated to include the latest information on LTE 3GPP standards Chapters on conformance testing have been majorly revised and there is an increased focus on LTE-Advanced Includes new sections on testing challenges as well as over the air MIMO testing, protocol testing and the most up-to-date test capabilities of instruments Written from both a technical and practical point of view by leading experts in the field

This book presents an alternative and simplified approaches for the robust adaptive detection and beamforming in wireless communications. It adopts several systems models including DS/CDMA, OFDM/MIMO with antenna array, and general antenna arrays beamforming model. It presents and analyzes recently developed detection and beamforming algorithms with an emphasis on robustness. In addition, simplified and efficient robust adaptive detection and beamforming techniques are presented and compared with exiting techniques. Practical examples based on the above systems models are provided to exemplify the developed detectors and beamforming algorithms. Moreover, the developed techniques are implemented using MATLAB—and the relevant MATLAB scripts are provided to help the readers to develop and analyze the presented algorithms. em style="mso-bidi-font-style: normal;"Simplified Robust Adaptive Detection and Beamforming for Wireless Communications starts by introducing readers to adaptive signal processing and robust adaptive detection. It then goes on to cover Wireless Systems Models. The robust adaptive detectors and beamformers are implemented using the well-known algorithms including LMS, RLS, IQRD-RLS, RSD, BSCMA, CG, and SD. The robust detection and beamforming are derived based on the existing detectors/beamformers including MOE, PLIC, LCCMA, LCMV, MVDR, BSCMA, and MBER. The adopted cost functions include MSE, BER, CM, MV, and SINR/SNR.

A comprehensive and invaluable guide to 5G technology, implementation and practice in one single volume. For all things 5G, this book is a must-read. Signal processing techniques have played the most important role in wireless communications since the second generation of cellular systems. It is anticipated that new techniques employed in 5G wireless networks will not only improve peak service rates significantly, but also enhance capacity, coverage, reliability , low-latency, efficiency, flexibility, compatibility and convergence to meet the increasing demands imposed by applications such as big data, cloud service, machine-to-machine (M2M) and mission-critical communications. This book is a comprehensive and detailed guide to all signal processing techniques employed in 5G wireless networks. Uniquely organized into four categories, New Modulation and Coding, New Spatial Processing, New Spectrum Opportunities and New System-level Enabling Technologies, it covers everything from network architecture, physical-layer (down-link and up-link), protocols and air interface, to cell acquisition, scheduling and rate adaption, access procedures and relaying to spectrum allocations. All technology aspects and major roadmaps of global 5G standard development and deployments are included in the book. Key Features: Offers step-by-step guidance on bringing 5G technology into practice, by applying algorithms and design methodology to real-time circuit implementation, taking into account rapidly growing applications that have multi-standards and multi-systems. Addresses spatial signal processing for 5G, in particular massive multiple-input multiple-output (massive-MIMO), FD-MIMO and 3D-MIMO along with orbital angular momentum multiplexing, 3D beamforming and diversity. Provides detailed algorithms and implementations, and compares all multicarrier modulation and multiple access schemes that offer superior data transmission performance including FBMC, GFDM, F-OFDM, UFMC, SEFDM, FTN, MUSA, SCMA and NOMA. Demonstrates the translation of signal processing theories into practical solutions for new spectrum opportunities in terms of millimeter wave, full-duplex transmission and license assisted access. Presents well-designed implementation examples, from individual function block to system level for effective and accurate learning. Covers signal processing aspects of emerging system and network architectures, including ultra-dense networks (UDN), software-defined networks (SDN), device-to-device (D2D) communications and cloud radio access network (C-RAN).

The digital front-end (DFE) is the most critical stage in a wireless base-station. The DFE along with the analog to digital converter (ADC) is responsible for bridging the analog RF and IF processing on one side and the digital baseband processing on the other side. The most important reason for replacing analog with digital signal processing is the ability to softly reconfigure the channels in the base station RF in real time, thus allowing for the implementation of various signal conditioning, compensation and mitigation channel non-linear responses. Once tested, these algorithms can be implemented on a proprietary CMOS vector processor and commercial FPGA hardware platforms. In this thesis, we attempt to minimize the design efforts and lower the cost involved in the usage of analog electronics by using sophisticated digital signal processing (DSP) for restoring and enhancing the quality of the wireless channels. This thesis presents a versatile Digital Front-End architecture, which has been simulated using MATLAB/Simulink. The architecture includes the design of robust Digital Up-Conversion (DUC) blocks in the transmit downlink and Digital Down-Conversion (DDC) blocks present in the receiver uplink paths in a wireless base station RF. Crest factor reduction (CFR) schemes help reduce the Peak to Average Power Ratio (PAPR)of the signal entering the base-station and have been implemented widely for code division multiple access (CDMA) and Long Term Evolution (LTE) systems, this is important because if the signal with the high PAPR is allowed to pass through the power amplifier(PA) it will result in the amplifier operating in its nonlinear region creating non-linear distortions in amplitude and phase, and the only other way to avoid this is to back off the signal to the linear region of the amplifier thus reducing its efficiency. The selection and design of the DUC and DDC filters has been compared and optimized to match to the spectral mask requirements mentioned in the 3GPP standards. Crest factor reduction has also been studied in detail and a computationally efficient algorithm for meeting the desired PAPR in accordance with the 3GPP standards will be presented. By using the CFR algorithm, the PAPR of the LTE

signal was reduced from 10.8 dB to 7 dB and from 10.5 dB to 8 dB for a WCDMA signal. Finally, we implement Digital Predistortion (DPD) which is a method by which one first stimulates a non-linear power amplifier (PA) with baseband samples and then observes the result of that stimulus at its output. Without this process we will need to use a power amplifier with a higher input power rating which needs to be backed off to operate in its linear region thus reducing the efficiency of the PA used and increasing its cost. The process involves the use of a digital predistorter which creates an expanding nonlinearity which when used in cascade with the PA nullifies the compressing nonlinear characteristics of the PA thus enabling its use in its linear region up to its saturating point. A Look-Up Table (LUT) type Adaptive Digital Pre-Distortion (ADPD) is presented; here we developed an algorithm where the output signal of the PA is used as a reference signal. This reference signal is then used to update the coefficients of the LUT, so that the non-linear responses of the PA will not the affect the amplified signals. In addition, we investigated methods such as the nonlinear auto-regressive moving average (NARMA) and the memory polynomial models. In the latter, the predistorter parameters are calculated from the output signal obtained from the PA through the adaptive functions obtained using the memory polynomial. From these parameters, the predistorted signal is reconstructed and fed to the input of the PA. By using the DPD algorithm the nonlinear distortions of the PA came down by 60 dB when a WCDMA signal was used and by around 40 dB when LTE signal was used. As the PA is the heart of the base-station RF, we show that the main function of the DFE is to ensure a PA linearized output with a high efficiency.

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A modern mobile phone is a highly complex electronic system made up from a variety of diverse sub-systems, all of which must work seamlessly together. Today's users have very high expectations which set tough demands on manufacturers as they introduce third generation technology. While quality, in terms of the phone's stability, performance and behaviour on the network, originate from good design, the only way to be sure of quality is by testing it. This makes testing a very important part of any mobile phone's life cycle, from development through to manufacture and beyond, touching a number of different disciplines and departments. Testing UMTS is divided in three sections. Section One provides an overview of major types of testing and the organisations and tasks involved. In particular it looks at what is involved in conformance testing and device certification. Section Two is more technical and looks at the UMTS standard itself, working through the protocol layers. Future trends and their impact on testing mobile devices are examined in Section Three, including the emergence of new technologies both in the access network and the core network and the evolution of new testing methodologies. Examines UMTS and the testing of UMTS devices which are huge areas in the testing process Provides essential information on processes and techniques for mobile phone testing Operation of the UMTS standard is described from a test point of view Focuses on most important areas of the 3rd-Generation Partnership Project (3GPP) standard from a test perspective Offers advice on products, services and resources that aid the testing process. This book is an ideal text for engineers and managers who are either directly involved in the process of testing UMTS mobiles, or who are looking for an understanding of what is involved in testing. Professionals involved in the development of UMTS mobiles, integration and verification, conformance testing, operator acceptance testing, manufacturing and servicing will find this book indispensable.

This book constitutes the thoroughly refereed conference proceedings of the 12th International Conference on Cognitive Radio Oriented Wireless Networks, CROWNCOM 2017, held in Lisbon, Portugal, in September 2017. The 28 revised full papers presented were carefully reviewed and selected from numerous submissions and cover the evolution of cognitive radio technology pertaining to 5G networks. The papers are clustered to topics on spectrum management; network management; trials, test beds, and tools; PHY and sensing; spectrum management.

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This book presents the proceedings of the International Computer Symposium 2014 (ICS 2014), held at Tunghai University, Taichung, Taiwan in December. ICS is a biennial symposium founded in 1973 and offers a platform for researchers, educators and professionals to exchange their discoveries and practices, to share research experiences and to discuss potential new trends in the ICT industry. Topics covered in the ICS 2014 workshops include: algorithms and computation theory; artificial intelligence and fuzzy systems; computer architecture, embedded systems, SoC and VLSI/EDA; cryptography and information security; databases, data mining, big data and information retrieval; mobile computing, wireless communications and vehicular technologies; software engineering and programming languages; healthcare and bioinformatics, among others. There was also a workshop on information technology innovation, industrial application and the Internet of Things. ICS is one of Taiwan's most prestigious international IT symposiums, and this book will be of interest to all those involved in the world of information technology.

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This book addresses challenges faced by both the algorithm designer and the chip designer, who need to deal with the ongoing increase of algorithmic complexity and required data throughput for today's mobile applications. The focus is on implementation aspects and implementation constraints of individual components that are needed in transceivers for current standards, such as UMTS, LTE, WiMAX and DVB-S2. The application domain is the so called outer receiver, which comprises the channel coding, interleaving stages, modulator, and multiple antenna transmission. Throughout the book, the focus is on advanced algorithms that are actually in use in modern communications systems. Their basic principles are always derived with a focus on the resulting communications and implementation performance. As a result, this book serves as a valuable reference for two, typically disparate audiences in communication systems and hardware design.

While 3G has been an outstanding success, the ever-growing demand for higher data rates and higher quality mobile

communication services continues to fuel conflict between the rapidly growing number of users and limited bandwidth resources. In the future, a 100-fold increase in mobile data traffic is expected. That will necessitate further improvements to 3GPP LTE (Long-Term Evolution) and create limitless opportunities for engineers who understand the technology and how to apply it to deliver enhanced services. Long Term Evolution: 3GPP LTE Radio and Cellular Technology outlines the best way to position yourself now for future success. With coverage ranging from basic concepts to current research, this comprehensive reference contains technical information about all aspects of 3GPP LTE. It details low chip rate, high-speed downlink/uplink packet access (HSxPA)/TDSCDMA EV 1x, LTE TDD, and 3G TDD. It introduces new technologies and covers methodologies to study the performance of frequency allocation schemes. The authors also discuss the proposed architecture of Mobile IPRR and distributed dynamic architecture in wireless communication, covering performance evaluation of the TD-SCDMA LTE System. With each passing day, more and more users are demanding mobile broadband data access everywhere, to facilitate synchronization of e-mails, Internet access, specific applications, and file downloads to mobile devices such as cell phones, smart phones, PDAs, and notebooks. LTE, successor to the 3G mobile radio network, is essential to creating radio coverage in the rollout phase and high capacity all over the radio cell in the long term. The 3GPP LTE will become increasingly crucial to supporting the high demand of data traffic rates generated by future mobile user terminals. Authored by international experts in the field, this practical book is an extremely valuable guide that addresses emerging current and future technologies associated with LTE and its future direction. Digital Signal Processing 101: Everything You Need to Know to Get Started provides a basic tutorial on digital signal processing (DSP). Beginning with discussions of numerical representation and complex numbers and exponentials, it goes on to explain difficult concepts such as sampling, aliasing, imaginary numbers, and frequency response. It does so using easy-to-understand examples and a minimum of mathematics. In addition, there is an overview of the DSP functions and implementation used in several DSP-intensive fields or applications, from error correction to CDMA mobile communication to airborne radar systems. This book is intended for those who have absolutely no previous experience with DSP, but are comfortable with high-school-level math skills. It is also for those who work in or provide components for industries that are made possible by DSP. Sample industries include wireless mobile phone and infrastructure equipment, broadcast and cable video, DSL modems, satellite communications, medical imaging, audio, radar, sonar, surveillance, and electrical motor control. Dismayed when presented with a mass of equations as an explanation of DSP? This is the book for you! Clear examples and a non-mathematical approach gets you up to speed with DSP Includes an overview of the DSP functions and implementation used in typical DSP-intensive applications, including error correction, CDMA mobile communication, and radar systems

"This book has collected the latest research within the field of real-time systems engineering, and will serve as a vital reference compendium for practitioners and academics"--Provided by publisher.

A one-stop Desk Reference, for R&D engineers involved in communications engineering; this is a book that will not gather dust on the shelf. It brings together the essential professional reference content from leading international contributors in the field. Material covers a wide scope of topics including voice, computer, facsimile, video, and multimedia data technologies * A fully searchable Mega Reference Ebook, providing all the essential material needed by Communications Engineers on a day-to-day basis. * Fundamentals, key techniques, engineering best practice and rules-of-thumb together in one quick-reference. * Over 2,500 pages of reference material, including over 1,500 pages not included in the print edition

Foreword from Arogyaswami Paulraj, Professor (Emeritus), Stanford University (USA) The first book to show how MIMO principles can be implemented in today's mobile broadband networks and components Explains and solves some of the practical difficulties that arise in designing and implementing MIMO systems Both theory and implementation sections are written in the context of the most recent standards: IEEE 802.11n (WiFi); IEEE 802.16 (WIMAX); 4G networks (3GPP/3GPP2, LTE)

This book introduces readers to a reconfigurable chip architecture for future wireless communication systems, such as 5G and beyond. The proposed architecture perfectly meets the demands for future mobile communication solutions to support different standards, algorithms, and antenna sizes, and to accommodate the evolution of standards and algorithms. It employs massive MIMO detection algorithms, which combine the advantages of low complexity and high parallelism, and can fully meet the requirements for detection accuracy. Further, the architecture is implemented using ASIC, which offers high energy efficiency, high area efficiency and low detection error. After introducing massive MIMO detection algorithms and circuit architectures, the book describes the ASIC implementation for verifying the massive MIMO detection. In turn, it provides detailed information on the proposed reconfigurable architecture: the data path and configuration path for massive MIMO detection algorithms, including the processing unit, interconnections, storage mechanism, configuration information format, and configuration method.

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This book constitutes the refereed proceedings of the 13th EAI International Conference on Cognitive Radio Oriented Wireless Networks, CROWNCOM 2018, held in Ghent, Belgium, in September 2018. The 20 revised full papers were selected from 26 submissions. The papers are organized thematically in tracks: Experimental, Licensed Shared Access and Dynamic Spectrum Access, and PHX and Sensing.